

ABSTRACT OF THE DISCLOSURE

A thin-film crystal wafer having a pn junction includes a first crystal layer of p GaAs, a second crystal layer of n $In_xAl_yGa_{1-x-y}P$, the first and second crystal layers being lattice-matched layers that form a heterojunction, and a control layer of a thin-film of $In_xAl_yGa_{1-x-y}P$ differing in composition from the n $In_xAl_yGa_{1-x-y}P$ of the second crystal layer is formed at the interface of the heterojunction. The control layer enables the energy discontinuity at the interface of the $In_xAl_yGa_{1-x-y}P/GaAs$ heterojunction to be set within a relatively broad range of values and thus enables the current amplification factor and the offset voltage to be matched to specification values by varying the energy band gap at the heterojunction.

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